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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No	
Filing Date	December 10, 2003
Inventor	H. Montgomery Manning et al.
Assignee	Micron Technology, Inc.
Group Art Unit	
Examiner	
Attorney Docket No	MI22-2295
Customer No	
Title: Semiconductor Constructions, and Me	thods of Forming Capacitor Devices

Assistant Commissioner for Patents P.O. Box 1450 Alexandria VA 22313-1450

CERTIFICATE OF FACSIMILE TRANSMISSION UNDER 37 CFR 1.8

I hereby certify that the following papers are being facsimile transmitted to the Patent and Trademark Office at (703) 872-9306 on the date shown below:

- 1. Transmittal Form (PTO/SB/21)
- 2. Supplemental Information Disclosure Statement with Form PTO-1449
- 3. Cited References (3)

Natalle King Telephone No. Facsimile No.

(**509) 624-42**76 (509) 838-3422

NUMBER OF PAGES IN FACSIMILE:

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PAGE 1/5 * RCVD AT 4/19/2005 5:36:00 PM [Eastern Daylight Time] * SVR:USPTO-EFXRF-1/7 * DNIS:8729306 * CSID:5098383424 * DURATION (mm-ss):01-52

* Received: 4 pages. * And not receive: page 515.

Under the P	apenwork Reduction Act of 1995	. no persons	U.S. Pater	PTC/SB/21 (03-03) Approved for use through 04/30/2003, OMB 0651-0031 at and Trademark Office; U.S. DEPARTMENT OF COMMERCE on of Information unless it displays a valid OMB control number.
			Application Number	10/733,181
T	RANSMITTAL		Filing Date	12/10/2003
	FORM		First Named Inventor	Manning et al.
(to be used for	r all correspondence after initial	filing)	Art Unit	2834
			Examiner Name	Unknown
Total Number of	of Pages In This Submission		Attorney Docket Number	MI22-2295
		ENCL	OSURES (Check all that	apply)
Amendm A A Extension Express Informati Certified Documer Response Incompte	nemittal Form Fee Attached Itent/Reply After Final Affidavits/declaration(s) In of Time Request Abandonment Request On Disclosure Statement Copy of Priority It(s) Ite to Missing Parts/ Ite Application Esponse to Missing Parts Index 37 CFR 1.52 or 1.53		Prawing(s) Icensing-related Papers Petition Petition to Convert to a Provisional Application Power of Attorney, Revocation Phange of Correspondence Addre Perminal Disclaimer Pequest for Refund D, Number of CD(s)	After Allowance Communication to a Technology Center (TC) Appeal Communication to Board of Appeals and Interferences Appeal Communication to TC (Appeal Notice, Brief, Reply Brief) Proprietary Information Status Letter Other Enclosure(s) (please Identify below): Form PTO-1449; Cited References (3)
T	SIGNAT	TURE OF	APPLICANT, ATTORNE	OP ACENT
Firm or Individual	David G. Latwesen, Ph.D., Wells St. John, P.S.			I, ON AGENT
Signature	Man			
Date	4/18/05			
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I hereby certify that thirst class mail in an	this correspondence is being fac envelope addressed to: Commi	simile trans ssioner for P	milted to the USPTO or deposited with latents, Washington, DC 20231 on this	the United States Postal Service with sufficient postage as s date: 4-19-05
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Signature				Date 4/19/05

This collection of information is required by 37 CFR 1 5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C.\122 and 37 CFR 1. N. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, Washington, DC 20231.

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No	
Filing Date	
Inventor	H. Montgomery Manning et al.
Assignee	Micron Technology Inc
Group Art Unit	2812
Examiner	Unknown
Attorney Docket No	MI22-2295
Customer No.	021567
Title: Semiconductor Constructions, and Mo	ethods of Forming Capacitor Devices

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

This Supplemental Information Disclosure Statement is being filed within three months of the filing date of the application or before the mailing of a first Office Action on the merits, whichever occurs last. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925.

Respectfully submitted.

Datad:

Bv:

David G. Latwesen, Ph.D.

Reg. No. 38,533

Sheet 1 of 2

Form PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. MI22-2295	SERIAL NO. 10/733,181
	LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)	APPLICANT: H. Montgome	ry Manning et al.
		FILING DATE December 10, 2003	GROUP 2834

"Examiner's Initials		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	6,204,143	3/2001	Roberts et al.			
	AB	6,180,450	1/2001	Dennison			
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	A	5,605,857	2/1997	Jost et al.			

l	Document Number	Date	Country	Class	Subclass	Trans	lation
 -		 				Yes	No
1 ~	DE 4447804 C2	1/24/2002	Germany (Abstract)				х
 AK							
 AL							

	AM	PCT/US2004/027898; Filed: 8/26/2004; PCT Search Report mailed 2/28/2005; 4 pp.
	AN	"Novel Robust Cell Capacitor (Leaning Exterminated Ring Type Insulator) And New Storage Node Contact (Top Spacer Contact) For 70nm DRAM Technology And Beyond"; Park et al.;
		2004 Symposium on VLSI Technology Digest of Technical Papers; 7/2004; pp. 34-35.
EXAMINE		DATE CONSIDERED